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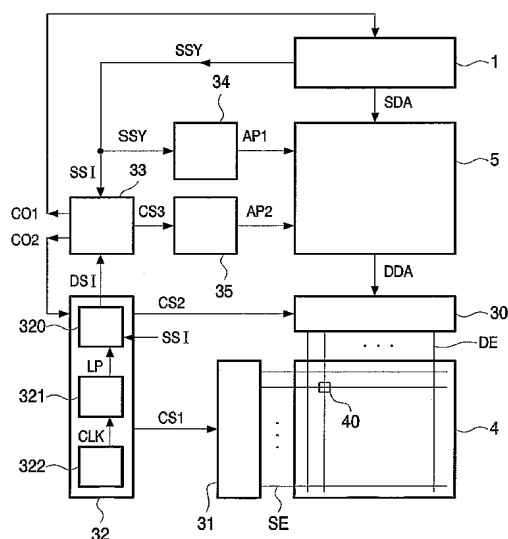
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(54) Title: DISPLAYING ON A MATRIX DISPLAY



(57) Abstract: A display method comprises the steps of generating (1) images comprising source data (SDA) and source frame synchronization instants (SSI) having a source frame rate (SFR). The source data (SDA) is stored (2) in a frame memory (5) under control of a first address pointer (AP1) having a start address (DSA) being determined by the source frame synchronization instants (SSI). During a read period (RP), display data (DDA) is read (2) from the memory (5) under control of a second address pointer (AP2) having a start address (SSA) being determined by display frame synchronization instants (DSI) having a display frame rate (DFR). The display data (DDA) is displayed (3) on a matrix display (4). The source frame rate (SFR) or the display frame rate (DFR) is controlled (2) to obtain, in a stable situation, the first address pointer (AP1) and the second address pointer (AP2) starting with an offset in time (TO) which has a fixed polarity during the read period (RP).

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